

Power Quality Enhancement in Micro Grids Using Fuzzy Hysteresis Current Controller

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Abstract: Electricity consumption is steadily rising, fossil fuels are depleting, and conventional plants are taking longer to erect, increasing the use of onsite renewable energy. Due to dynamic changes in environmental factors and customer load, renewable energy generation is experiencing reliability and quality issues. All of these issues are being addressed by developing integrated renewable energy cluster microgrids in urban communities worldwide. However, a microgrid's effectiveness in generating quality power depends on the power electronic converters it uses. Thus, this paper proposes the "Fuzzy Hysteresis Current Controller (FHCC)-based Inverter" for improving the power quality in renewable energy integrated cluster microgrids that are operated either in grid-connected or autonomous mode. Here, the inverter is controlled through a fuzzy logic-based hysteresis current control loop, achieving superior performance. System modelling and simulations are done using MATLAB/Simulink®. The performance analysis of the proposed and conventional inverter configurations is done by computing various power quality indices, namely voltage characteristics (swell, sag, and imbalance), frequency characteristics (deviations), and total harmonic distortion. According to IEEE/IEC/EN global standards for renewable energy integrated cluster microgrids, the proposed FHCC-based inverter achieves a better power quality than the traditional ST-PWM-based inverter.

Key words: Fuzzy logic; Hysteresis current control; Microgrids; Power quality;

1.0 Introduction

Energy is a key component of any country's economic development. In the twenty-first century, people in both urban and rural areas around the world have higher and more sophisticated demands for their quality of life. Urbanization is expected to account for 70% of the world population by 2050, resulting in huge use of existing energy sources [1,2]. Therefore, moving towards sustainable cities is a political and significant objective for many countries given population expansion and recurrent energy crises [3]. Green energy is to be extracted from accessible renewable energy sources while keeping diverse customer demands and environmental issues in mind [4]. As a result, the inspiration for DG-based microgrids has been in the works for several years. Originally, these microgrids were designed to give power to places that have limited access to the electricity. Microgrids are designed to operate in both autonomous and grid-connected

modes. With the increased penetration of DGs into the distribution system, several power quality issues, such as voltage swells/sags, voltage waveform shape, voltage flickers, frequency deviations, and total harmonic distortion (THD), are emerging on both the customer and grid sides, particularly when the microgrid is connected to the utility grid [5]. For improving the power quality of microgrids, researchers have created different power devices, such as the “Dynamic Voltage Restorer (DVR)”, “Automatic Voltage Regulator (AVR)”, “Distributed Static Compensator (D-STATCOM)”, and “Unified Power Quality Controller (UPQC)”. To address the aforementioned power quality issues, research is primarily focused on three aspects: (1) inverter topologies controlled by modern modulation techniques, (2) alternative conversion methods to replace power electronic inverters, and (3) taking uncertainty into account while designing storage units for energy management. The following is a summary of the state-of-the-art literature on increasing the power quality of distribution power systems.

In [6], the authors proposed the model predictive control methodology to maintain the power quality of microgrids, which regulates the microgrids’ power converters to meet the criteria. The control algorithm is designed to function with the following microgrids: connected to the grid, islanded, and interconnected. To avoid power quality degradation, a hybrid differential evolution optimization and artificial neural network is utilized to restore system voltage and frequency while considering a wide variety of disturbances [7]. Reference [8] reviewed different control mechanisms for improving power quality in microgrid systems that have been studied and addressed. The same is also expanded to address various filters, power quality compensators, and optimization strategies for improving power quality in microgrids. In [9], the authors discussed the D-STATCOM (Distribution Static Synchronous Compensator), which is used as an inverter-based power quality conditioner to improve power quality in distribution networks. A nonlinear and resilient fuzzy-PI controller is suggested for controlling D-direct STATCOM’s and quadrature axis currents. In addition, the authors presented how the D-fuzzy STATCOM’s logic controls and increases the damping of a power system. Later, in [10], the authors discussed how distributed FACTS in smart grids with renewable energy sources play a vital role in optimizing the power factor, energy utilization, upgrading power quality, assuring effective energy consumption, and energy management. The authors also reviewed FACTS technology tools and applications for improving power quality and maximizing the use of renewable energy systems. In [11], the authors used a chopper, an inverter, and a typical PI controller to improve the power quality of a fuel cell connected to the power network. Two PI controllers are used; each one was tuned using one of the three contemporary evolutionary computing techniques, namely Harmony Search (HS), Modified Flower Pollination Algorithm (MFPA), and Electromagnetic Field Optimization (EFO). A unique artificial neural network based control solution was proposed to minimize the influence of voltage deviation, sag/swell, unbalancing, frequency, total harmonic distortion, and power factor following IEEE/IEC standards [12].

Moreover, in [13], a comprehensive survey of power quality enhancement devices was conducted with a focus on auxiliary services provided by multi-functional DGs. A literature review of the microgrid concept, testbeds, and related control approaches

was also conducted. Although certain DGs were used to increase the supply quality, these applications were not designated as multi-functional DGs. This contains a series transformer whose secondary impedance is regulated indirectly by voltage injection. This “smart branch” can be regulated adaptively. It is flexible and uses a finite control set-model predictive controller. In addition to inverter topologies, certain optimization strategies for sending quality power to the consumer’s side were considered in this literature. In this regard, the authors in proposed an improved current control strategy for a multilayer converter. In online harmonic compensation applications in microgrids, the control method will overcome earlier control systems’ inefficiency in tracking harmonic current. This control method works with multi-functional DG converters that are grid-connected.

The work cited above mainly focuses on power quality issues in a single micro grid that is either autonomous or grid-connected, and solutions to interoperable multi-micro grid systems are very limited. Furthermore, the majority of these solutions did not consider all aspects of the power quality issue. Further, the constant growth of regulations governing how energy is created and consumed necessitates the continuing search for innovative and effective solutions. In light of all of these considerations, this paper:

- Proposes the idea of interoperating many adjacent micro grids in an urban energy community to form a renewable-energy-based micro grid cluster This increases the energy availability, thereby improving the power supply reliability by allowing the cluster to manage its own energy requirement rather than relying on the utility grid;
- Proposes a new inverter control mechanism, namely “Fuzzy Hysteresis Current Controller-based Pulse Width Modulation (FHCC-PWM)”, which improves the power supply quality The proposed fuzzy logic improves the control loop ability to regulate the system under variety of operating conditions.

Based on this objective, the rest of the paper is organized as follows: Section 2 describes the system of cluster micro grids with a description of conventional and proposed inverters, their other constituents, and various performance measures. Section 3 describes the concept and implementation of the proposed FHCC. Section 4 provides the results and their corresponding discussion, followed by conclusions in Section 5.

2.0 Description of the Components Present in Cluster Microgrids

The general architecture of the cluster micro grid system under consideration is depicted in Figure 1, which is made up of four separate green buildings with varied load profiles: residential, software, academic institute, and manufacturing enterprises. Each building is referred to as a single micro grid and is connected to locally available renewable energy sources, such as photovoltaic, wind turbines, and fuel cells, as well as storage batteries, DC/DC converters [11], power electronic-based inverters, and local building loads.

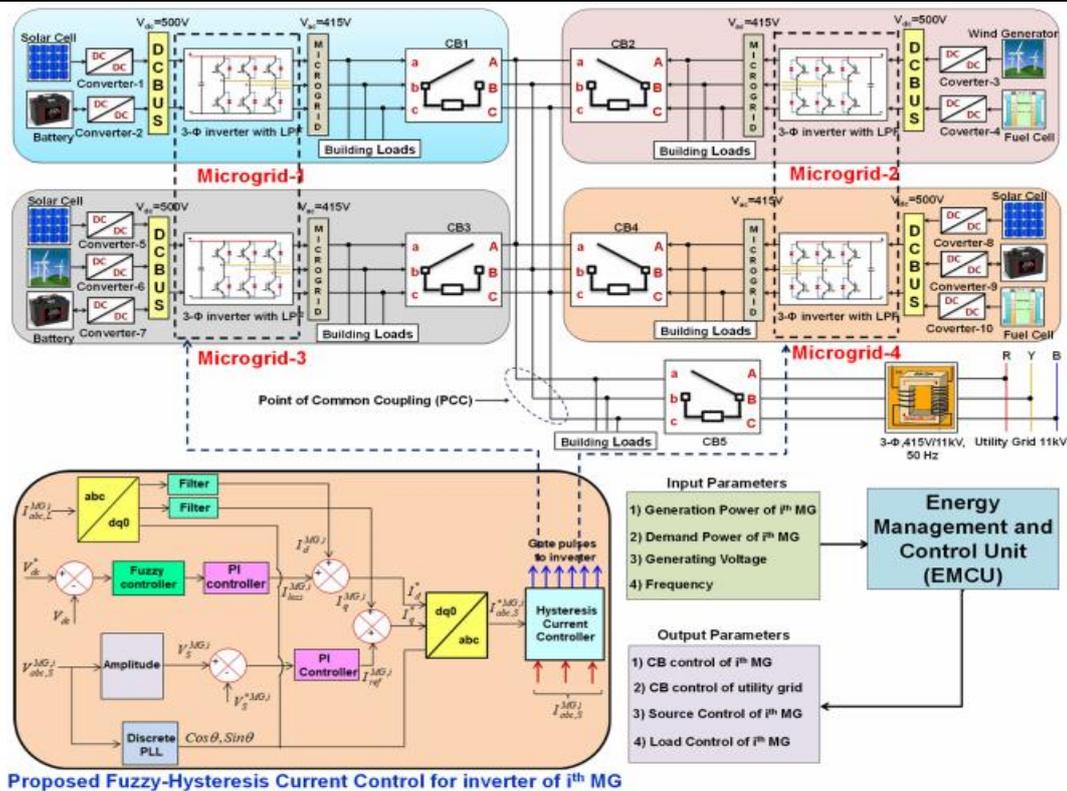


Figure 1: Elucidation of urban community cluster micro grids, including the proposed FHCC.

Micro grid 1 (residential building) consists of a solar photovoltaic and battery storage system with a generation capacity of 4.5 kW to supply a local base load of 40 kVA. Micro grid 2 (software building) consists of wind sources and fuel cells as the energy sources with the generation capacity of 2.5 kW to supply the local building loads of 38.5 kVA. Micro grid 3 (academic institute building) consists of solar photovoltaic, wind sources as the energy sources, and a battery as a storage system with a generation capacity of 4.25 kW to supply the local building loads of 57 kVA. Micro grid 4 (manufacturing enterprises building) consists of solar photovoltaic, fuel cells as the energy sources, and also consists of a battery as a storage system with a generation capacity of 4 kW to supply the local building loads of 60 kVA. A DC bus is modeled in this work to supply 500 V DC voltages to the inverter to produce a corresponding AC voltage of 415 V at the output. The proposed micro grid cluster requires an energy management and control unit (EMCU) to make the power transactions with adjacent micro grids/utility grid during power excess/deficit conditions. In this cluster, four micro grids are interconnected with interoperability, where each micro grid in the cluster is associated with a local agent controller that communicates with neighborhood micro grids and also with the utility grid for energy transactions through EMCU.

2.1 Description of ST-PWM Based Multilevel Inverter

The power electronic inverter is crucial in renewable energy-based power systems. In general, two-level inverters are widely used in many industrial applications, such as traction drives, renewable energy integration, and also in high-voltage DC transmission.

However, these two-level inverters are facing some serious issues, such as high switching frequencies, high switching losses, and also non-availability of high power devices. These issues are overcome by using multilevel inverters. Hence, the RES-generated DC power is fed to AC loads via a traditional multi-level inverter, which is connected to the point of common coupling (PCC). In this work, a three-phase, five-level diode clamped inverter is modeled for the analysis of an interconnected micro grid clustering system considered for the study. It consists of four DC-link capacitors and 28 switching devices that operate at a switching frequency of 100 kHz. The main function of clamping diodes is to obtain clamped voltage with different DC voltage levels. For creating required switching pulses, the inverter in a cluster micro grid system is first developed using sinusoidal triangular pulse width modulation (ST-PWM) control. The configuration, generation of gate pulses for one leg, and the output voltage per phase of the ST-PWM-based multilevel inverter are shown in Figure 2. Similarly, the switching logic for its operation is given in Table 1.

S_1	S_2	S_3	S_4	S_5	S_6		S_7	S_8	V_{AD}	V_{AN}
1	1	1	1	0	0		0	0	V_{dc}	$V_{dc}/2$
0	1	1	1	1	0		0	0	$3V_{dc}/4$	$V_{dc}/4$
0	0	1	1	1	1		0	0	$V_{dc}/2$	0
0	0	0	1	1	1		1	0	$V_{dc}/4$	$-V_{dc}/4$
0	0	0	0	1	1		1	1	0	$-V_{dc}/2$

2.2 Description of Proposed FHCC Based Inverter

The multi-level inverter is sufficient for improving power quality, but as we get to higher levels, switching losses grow, the control structure becomes more complex, and the system becomes bulky and more expensive. All of these issues are reduced with the suggested inverter, which is controlled by machine learning technique, namely “Fuzzy Logic-based Hysteresis Current Controller (FHCC)”, to control the multi-level inverter for improved power quality. The proposed inverter is designed as a current-controlled voltage source converter, reducing the need for clamping diodes and capacitors. A detailed description of this suggested controller is explained in Section 3.

2.3 Description of Energy Management Control Unit (EMCU)

An energy management control unit (EMCU) is an information system on a software platform that supports the functionality of providing low-cost generation, transmission, and distribution of electrical energy, according to the international standard IEC 61970. In micro grids, energy management is a computer-based control strategy that ensures the system’s best performance. A micro grid must optimize the use of renewable energy sources in a variety of ways. The EMCU components that carry out decision-making procedures include machine interfaces and SCADA (Supervisory Control and Data Acquisition Systems). Therefore, in this article, an effective EMCU called two-layer energy management control is adopted, which is incorporated in the proposed cluster micro grid system for managing the resources connected in all micro grids. This makes

smooth energy transactions and also maintains grid frequencies under various loading conditions.

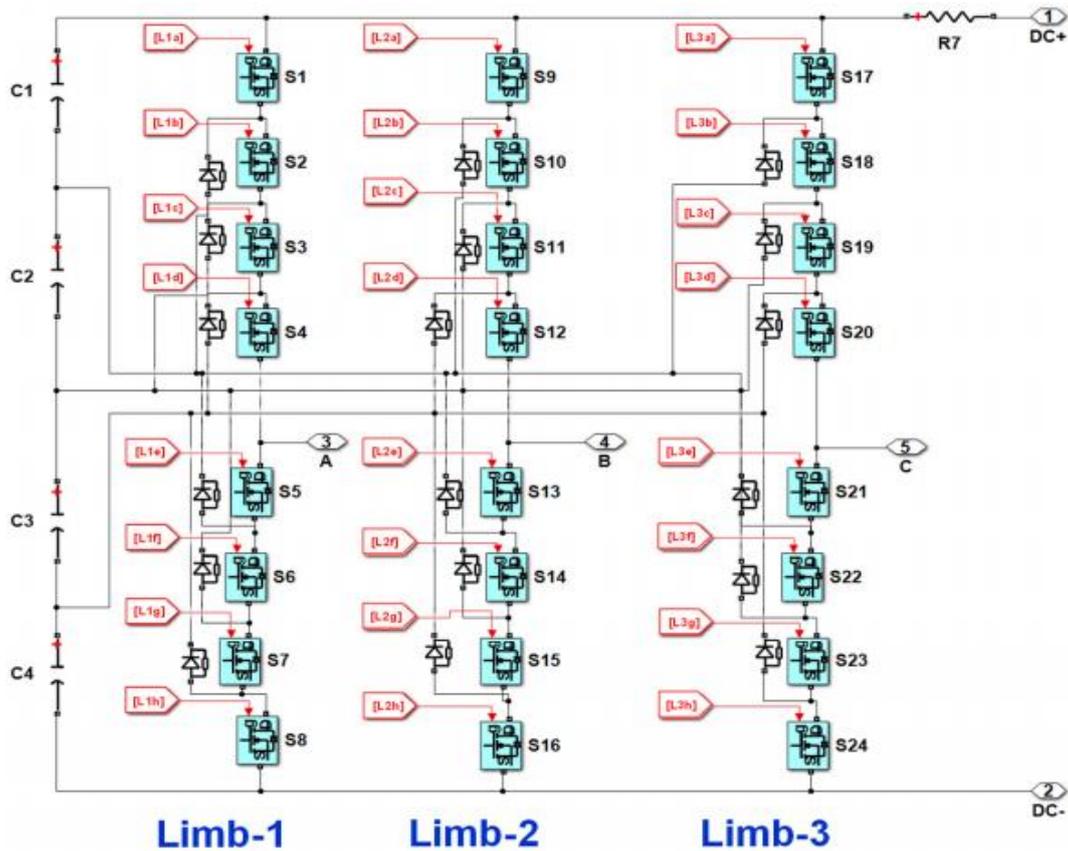


Figure:2 Multilevel inverter configuration, gate pulse generation for one limb, and output voltage. Shows the configuration of multilevel inverter based on ST-PWM;

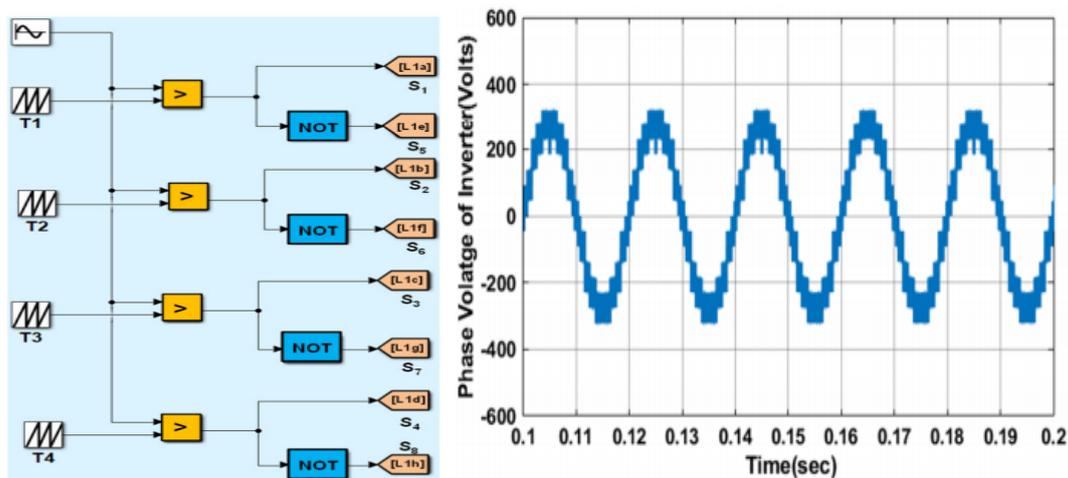


Figure 3(a) generation of gate pulses for one leg; (b) the output voltage per phase of the ST-PWM-based multilevel inverter

3.0 Performance Issues and Measures

With the high penetration of DG sources in the distribution grid, power electronic based inverters play a very important role. These power electronic systems have nonlinear characteristics. Due to this non-linearity, several power quality issues appear on the

grid side and also at the consumer premises. Therefore, in grid-connected renewable energy based micro grids, the major challenge is to keep the power quality indices in acceptable ranges, i.e., voltage characteristics such as sag/swell should be in the range of 40% as per IEC61000-4-11, voltage imbalances should be in the range of $\pm 3\%$ as per IEEE 1159.3 and EN 50160, frequency deviations are in the range $\pm 2\%$ as per IEC 61727 and IEC 61000-2-2, and harmonic distortion is in the range of 5% as per IEEE 1547.1 and IEEE-519.

Voltage sag/swell: Root mean square (RMS) value of voltage ($V \times \text{RMS}$) is calculated by squaring all the sampled voltages and is averaged over a window with a duration of one cycle at the sampling instant x given in Equation (1). Voltage sag and swell are the drop and rise that are observed in the RMS value of the voltage that occurs due to sudden rise and fall of the load, respectively

Voltage imbalance: It is defined as the ratio of negative sequence voltage to positive sequence voltage expressed in terms of the percentage given in Equation (2)

Frequency variations: The load frequency is to be continuously monitored and maintained close to 50 Hz with the penetration of DG sources in micro grids and is given in Equation (3).

Total harmonic distortion (THD): THD is the measure for the level of distortion (harmonic) present in a three-phase power system. It is calculated by measuring the ratio between the amplitudes (RMS value) of a set of total higher-frequency components with respect to harmonics and the value at fundamental frequency components given in Equations (4) and (5)

$$V_{RMS}^x = \sqrt{\frac{1}{y} \sum_{k=1+i-y}^i V_k^2} \text{-----(1)}$$

$$\text{Voltage imbalance (\%)} = \frac{\text{voltage-ve seq}}{\text{voltage+ve seq}} \times 100 \text{-----(2)}$$

$$\Delta f = -(\Delta P_{MG}^1 + \Delta P_{MG}^2 + \Delta P_{MG}^3 + \Delta P_{MG}^4) \rho \text{-----(3)}$$

$$THD^{Voltage} = \frac{\sqrt{\sum_{i=2}^{\infty} (V_i^{rms})^2}}{V_{fund}^{rms}} \text{-----(4)}$$

$$THD^{Voltage} = \frac{\sqrt{\sum_{i=2}^{\infty} (I_i^{rms})^2}}{I_{fund}^{rms}} \text{-----(5)}$$

where y is the number of samples per cycle, V_k is the recorded voltage waveform sample, ρ is the frequency droop coefficient of cluster microgrids, and ΔP_i MG (for $i = 1, 2, 3, 4 \dots$) is the real power change in the i th microgrid; $V_{rms i}$ and $I_{rms i}$ are the voltage and current components corresponding to n th harmonic, respectively. Similarly, the voltage and current components with respect to the fundamental frequency of i th microgrid are $V_{rms fund}$ and $I_{rms fund}$.

3.1 Proposed Fuzzy Hysteresis Current Controller (FHCC)

The proposed control circuit for producing switching pulses for the inverter is shown in Figure 3. The philosophy involved is explained in the following two subsections. Section 3.1 examines the generation of reference current using SRF theory and fuzzy logic, while Section 3.2 discusses the development of switching gate pulses using a hysteresis current controller, which generates gate pulses to the inverter of the i th microgrid.

Reference (Source) Current Generation

The reference current is obtained from the voltage (source) and current (load) of ith microgrid. The set of voltages and currents are transformed $\alpha - \beta - o$ frame using Equations (6) and (7) . The d-q (direct and quadrature) reference frame is obtained from the angle with respect to $\alpha - \beta - o$ frame.

$$\begin{bmatrix} \vec{V}^{MG,i} \\ \vec{V}_{MG,i}^0 \\ \vec{V}_{MG,i\beta}^\alpha \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} \vec{V}_{MG,i}^a \\ \vec{V}_{MG,i}^b \\ \vec{V}_{MG,i}^c \end{bmatrix} \text{-----(6)}$$

$$\begin{bmatrix} \vec{I}^{MG,i} \\ \vec{I}_{MG,i}^0 \\ \vec{I}_{MG,i\beta}^\alpha \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} \vec{I}_{MG,i}^a \\ \vec{I}_{MG,i}^b \\ \vec{I}_{MG,i}^c \end{bmatrix} \text{-----(7)}$$

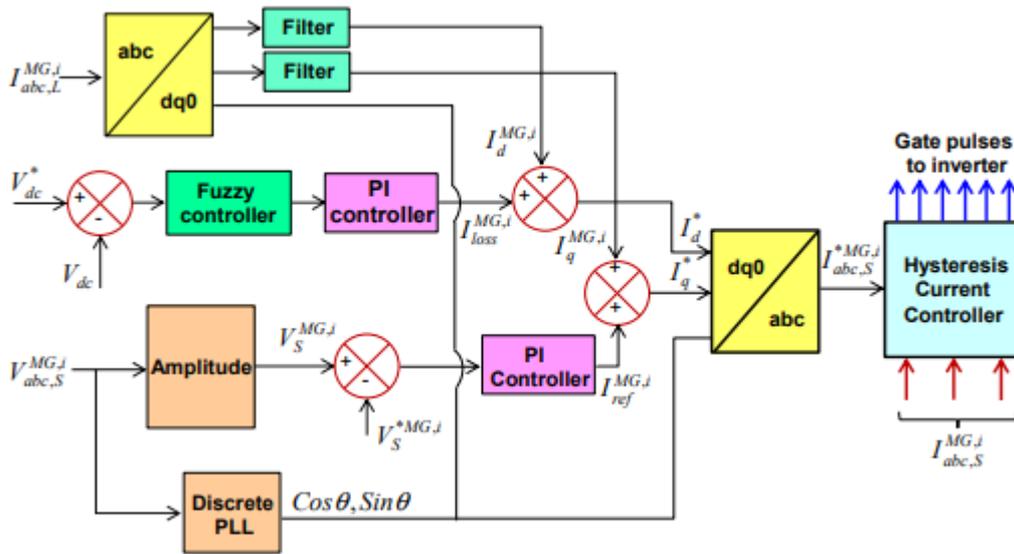


Figure 4. Proposed FHCC circuit for operating the inverter of ith microgrid.

Here, $\vec{I}^{MG,i}$ abc and $\vec{V}^{MG,i}$ abc are the source currents and voltages with respect to a - b - c frame, and $*\vec{I}^{MG,i}$ $\alpha\beta$, $*\vec{V}^{MG,i}$ $\alpha\beta$ are the source currents and voltages with respect to $\alpha - \beta - o$ frame. The transformation of $\alpha - \beta - o$ frame to d - q - o frame is given in Equation (8).

$$\begin{bmatrix} \vec{I}^{MG,i} \\ \vec{I}_{MG,i}^a \\ \vec{I}_{MG,i}^b \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & \cos \theta & \sin \theta \\ 0 & -\sin \theta & -\cos \theta \end{bmatrix} \begin{bmatrix} \vec{I}_{MG,i} \\ \vec{I}_{MG,i}^0 \\ \vec{I}_{MG,i\beta}^\alpha \end{bmatrix} \text{-----(8)}$$

The reference current with respect to d-axis ($I^* d$) was obtained as given in Equation (9).

$$\vec{I}_d^* = \vec{I}_d^{MG,i} + \vec{I}_{loss}^{MG,i} \text{-----(9)}$$

Where $\vec{I}_d^{MG,i}$ are the d-axis current, and the loss component value at pth sampling instant $\vec{I}_{loss(p)}^{MG,i}$ for meeting the losses in ith microgrid is obtained as given in Equation (10).

$$\vec{I}_{loss(p)}^{MG,i} = \vec{I}_{loss(p-1)}^{MG,i} + m_{pd} [(\vec{V}_{dc}^* - \vec{V}_{dc})_p - (\vec{V}_{dc}^* - \vec{V}_{dc})_{(p-1)}] + m_{id} (\vec{V}_{dc}^* - \vec{V}_{dc})_{(p)} \text{-----(10)}$$

Where, m_{pd} m_{id} are PI controller gains and are tuned by fuzzy logic rules. The real power supplied by the source should be equal to the real power demand of the load in the steady state plus a modest amount to compensate for filter losses. As a result, the DC capacitor voltage can be kept constant. In this study, a fuzzy controller is introduced to the d-axis in the d-q frame to effectively manage the active current component to keep the DC link voltage constant.

The planned fuzzy controller will set the PI controller's parameters in such a way that it is utilized to manage a small amount of active current, which is subsequently regulated by the current controller to keep the DC-link capacitor voltage constant. Similarly, the reference current with respect to q-axis (\vec{I}_q^*) is obtained as Equation (11).

$$\vec{I}_q^* = \vec{I}_q^{MG,i} + \vec{I}_{qref}^{MG,i} \text{-----(11)}$$

Here, $\vec{I}_q^{MG,i}$ is the q-axis current, and $\vec{I}_{qref}^{MG,i}$ is the output component produced by the PI controller. The magnitude of the terminal voltage of ith microgrid at PCC is calculated as given in Equation (12), and the q axis reference current at p th sampling instant of ith microgrid is given in Equation (13).

$$\vec{V}_t^{MG,i} = (\frac{2}{3}(\vec{V}_{as}^2 + \vec{V}_{bs}^2 + \vec{V}_{cs}^2))^{\frac{1}{2}} \text{-----(12)}$$

$$\vec{I}_{qref(p)}^{MG,i} = \vec{I}_{qref(p-1)}^{MG,i} + m_{pq}\{(\vec{V}_{te}^{*MG,i})_p - (\vec{V}_{te}^{*MG,i})_{(p-1)}\} + m_{iq}(\vec{V}_{te}^{*MG,i})_{(p)} \text{-----(13)}$$

where $\vec{I}_{qref(p)}^{MG,i}$ is the q-axis current at p th sampling instant; $\vec{V}_{te(p)}^{*MG,i} = \vec{V}_t^{*MG,i} - \vec{V}_t^{MG,i}$ is terminal voltage amplitude of ith microgrid at p th sampling instant obtained by taking the difference between measured value ($\vec{V}_t^{MG,i}$) and reference value ($\vec{V}_t^{*MG,i}$); and m_{pq} , m_{iq} are PI controller gains. The reference current can be obtained as given in Equation (14).

$$\begin{bmatrix} I_{os}^{*MG,i} \\ I_{\alpha s}^{*MG,i} \\ I_{\beta s}^{*MG,i} \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & \cos \theta & -\sin \theta \\ 0 & \sin \theta & \cos \theta \end{bmatrix} \begin{bmatrix} 0 \\ I_d^{*MG,i} \\ I_q^{*MG,i} \end{bmatrix} \text{----- (14)}$$

By applying the reverse transformation to the above current vector, we then obtain the reference source vector as given in Equation (15).

$$\begin{bmatrix} \vec{I}_{MG,i} \\ \vec{I}_{MG,i}^{\alpha s} \\ \vec{I}_{MG,i}^{* \beta s} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 0 & 1 & 0 \\ 0 & -\frac{1}{2} & \frac{\sqrt{3}}{2} \\ 0 & -\frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} \vec{I}^{*MG,i} \\ \vec{V}_{MG,i}^{* \alpha s} \\ \vec{V}_{MG,i}^{* \beta s} \end{bmatrix} \text{----- (15)}$$

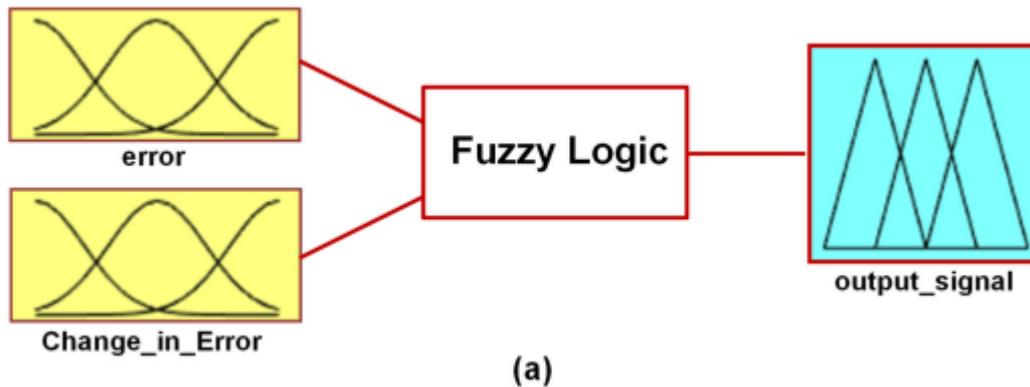
where $\vec{I}^{*MG,i} \alpha \beta, s$ and $\vec{I}^{*MG,i} \alpha \beta, s$ are the reference source currents with respect to a - b - c and $\alpha - \beta - o$ frames. The scheme of fuzzy logic (fuzzification) is explained as follows.

- Scheme of fuzzy logic: In this scheme, the fuzzy controller of the proposed inverter is implemented by considering and evaluating some linguistic rules. The internal process of fuzzy controller is explained as follows. The input error value is calculated by taking the difference between the reference voltage (V^*_{dc}) and sensed voltage (V_{dc}). Here, both input signals, i.e., $e(n)$ and $\Delta e(n)$, are numerical variables and are transformed to linguistic variables by considering the following fuzzy sets as given. The characterization of fuzzy logic is as follows:

1. Consists of seven fuzzy sets (N-3, N-2, N-1, E0, P1, P2, and P3);
 2. For simplicity, the triangular membership function is considered;
 3. Mamdani fuzzy inference mechanism is used;
 4. "Centroid method" is used for defuzzification.
- Fuzzification: In this process, instead of numerical values, fuzzy uses linguistic variables. In the system, the error signal can be assigned to negative large (N-3), negative medium (N-2), negative small (N-1), extreme zero (E0), positive small (P1), positive medium (P2), and positive large (P3). This process converts numerical variables to linguistic variables (fuzzy numbers), and the surface plot of the fuzzy logic controller is shown in Figure 4.
 - Rule elevation: Basic operations of fuzzy logic are needed to evaluate fuzzy set rules shown in Figure 5, which are obtained by considering "union", "intersection", and "complement" functions. Considering two fuzzy sets (- M and - N), the universe A and the following Equations (16)–(18) are the basic relations performed on fuzzy sets.
 Union operation $\mu_{M \cup N}(x) = \mu_M \vee \mu_N, \forall x \in A$ (16)
 Intersection $\mu_{M \cap N}(x) = \mu_M \wedge \mu_N, \forall x \in A$ (17)
 Complement operation $\mu_{\bar{M}} = 1 - \mu_M(x), \forall x \in A$ (18)

Process of getting defuzzified output: With the process of defuzzification, a fuzzy set is converted to its crispest version. The mathematical expression for obtaining defuzzified output x^* is as given in Equation (19).

$$x^* = \frac{\int \mu_{\bar{M}}(x).x.dx}{\int \mu_{\bar{M}}(x).x} \dots\dots\dots (19)$$



(a)
Figure 5. Proposed FHCC schematic Fuzzy errors

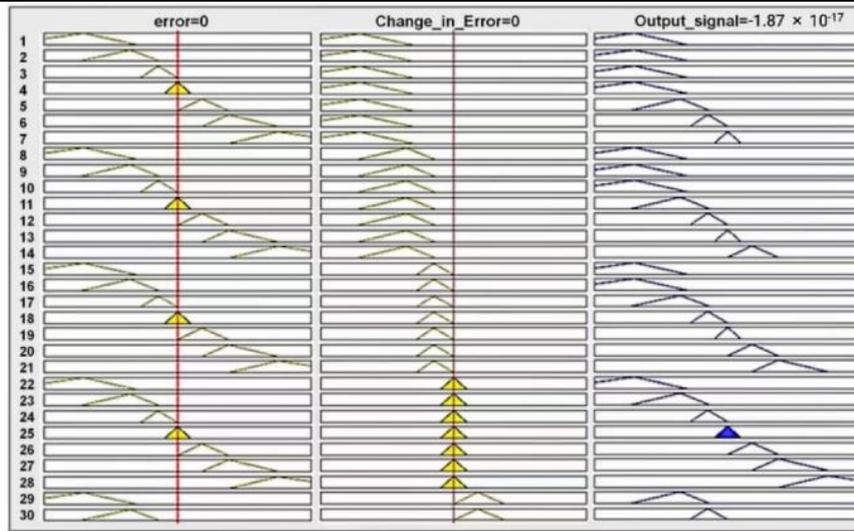


Figure 6. Fuzzy logic controller rules (sample 30 rules shown).

- Rule matrix: It is used to represent fuzzy sets and operators in the form of conditional statements. In this fuzzy logic controller, a total of 49 rules are used, and the list is given in Table 2. The fuzzy membership functions are shown in Figure 6.

Table 2. Rules Proposed for the Implementation of the Fuzzy Logic.

$e(n)$	$\Delta e(n)$	N-3	N-2	N-1	E_0	P_1	P_2	P_3
N-3	N-3	N-3	N-3	N-3	N-3	N-2	N-1	N-3
N-2	N-2	N-2	N-3	N-3	N-2	N-1	E_0	N-2
N-1	N-3	N-3	N-2	N-1	E_0	P_1	P_2	N-1
E_0	N-3	N-2	N-1	E_0	P_1	P_2	P_3	E_0
P_1	N-2	N-1	E_0	P_1	P_2	P_3	P_3	P_1
P_2	N-1	E_0	P_1	P_2	P_3	P_3	P_3	P_2
P_3	E_0	P_1	P_2	P_3	P_3	P_3	P_3	P_3

Gate Pulses Generation from Hysteresis Current Controller

The hysteresis current controller can be implemented to generate the switching pulses to the inverter to obtain a faster response and is connected in each phase independently. The conventional hysteresis current controller used in this work is shown in Figure 7. The actual current $I_{MG,i act}(t)$ is compared with the reference current $I_{MG,i ref}(t)$ of i th microgrid, and the resultant error current signal is passed through the hysteresis current controller to produce the switching gate signals to the proposed inverter. The logic behind the generation of switching pulses of the inverter of the i th microgrid is given as follows:

1. If $I_{MG,i act}(t) < (I_{MG,i ref}(t) - HB)$, then inverter upper switch is OFF, and lower switch is ON for leg corresponding to phase A of the i th microgrid;
2. If $I_{MG,i act}(t) > (I_{MG,i ref}(t) + HB)$, then inverter upper switch is ON, and lower switch is OFF for leg corresponding to phase A of the i th microgrid.

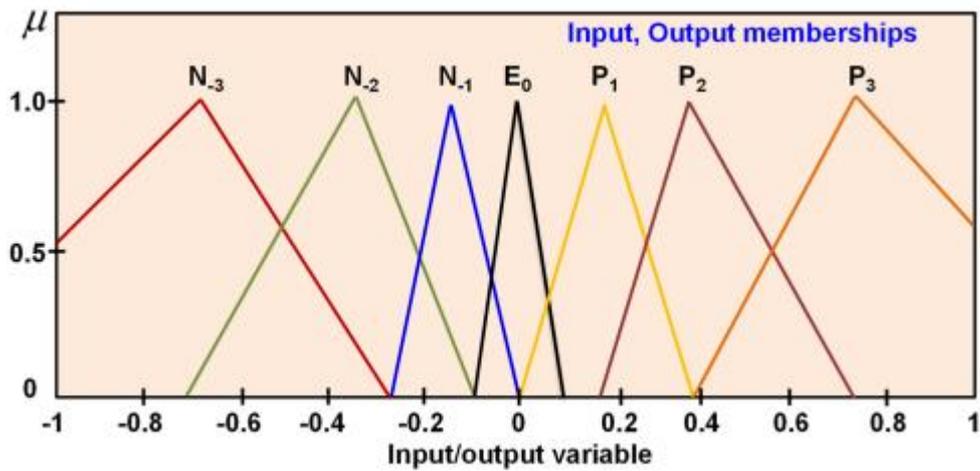


Figure 7. Fuzzy logic membership functions for both input and output variables.

If the error signal in phase A crosses the upper band limit of hysteresis, then the upper switch of the inverter corresponding to phase A is OFF, and the lower switch is ON. Due to this action, current starts decaying. If the error signal in phase A crosses the hysteresis lower band limit, then the lower switch of the inverter arm corresponding to phase A is turned OFF, and the upper switch is turned ON. As a result, the current again returns into the hysteresis band. Similarly, the logic for the remaining two phases can be determined

The geometry of the hysteresis controller is shown in Figure 7. From this, the rising edge currents and falling edge currents are computed as given in Equations (20) and (21), respectively, from which the hysteresis band (HB) can be calculated as in Equation (22). This is obtained by considering switching intervals t_1 and t_2 as shown in Figure 7, where L_a is phase inductance, $(I_{as}^{MG,i})^+$ and $(I_{as}^{MG,i})^-$ are rising and falling segments of current. From Figure 7, the following relations can be written as given in Equations (23) and (24)

$$\frac{d(I_{as}^{MG,i})^+}{dt} = \frac{1}{L_a} (V_{dc} - V_{as}^{MG,i}) \dots\dots\dots (20)$$

$$\frac{d(I_{as}^{MG,i})^-}{dt} = \frac{1}{L_a} (V_{dc} + V_{as}^{MG,i}) \dots\dots\dots (21)$$

$$t_1 \cdot \frac{d(I_{as}^{MG,i})^+}{dt} - t_1 \frac{d(I_{as}^{*MG,i})}{dt} = 2H_B \dots\dots\dots (22)$$

$$t_2 \cdot \frac{d(I_{as}^{MG,i})^-}{dt} - t_2 \frac{d(I_{as}^{*MG,i})}{dt} = -2H_B \dots\dots\dots (23)$$

$$t_1 + t_2 = \tau_c = \frac{1}{f_c} \dots\dots\dots (24)$$

Here, t_1 and t_2 are switching intervals, and f_c is the modulation frequency. By adding Equations (22) and (23) and substituting Equation (24), we arrive at Equation (25). Similarly, by substituting Equations (20)–(22) in Equation (25) and after simplification, we obtain Equation (26).

$$t_1 \cdot \frac{d(I_{as}^{MG,i})^+}{dt} - t_2 \frac{d(I_{as}^{MG,i})^-}{dt} - (t_1 - t_2) \frac{d(I_{as}^{*MG,i})}{dt} = 0 \dots\dots\dots (25)$$

$$t_1 - t_2 = \frac{L_a}{V_{dc} f_c} \left(\frac{V_{as}^{MG,i}}{L_a} + \frac{dI_{as}^{*MG,i}}{dt} \right) \dots\dots\dots (26)$$

By subtracting Equation (23) from Equation (22), we derive Equation (27), and by substituting Equations (20), (21), and (24) in Equation (27), after simplification, we obtain Equation (28).

$$t_1 \cdot \frac{d(I_{as}^{MG,i})^+}{dt} - t_2 \cdot \frac{d(I_{as}^{MG,i})^-}{dt} - (t_1 - t_2) \frac{d(I_{as}^{*MG,i})}{dt} = 4H_B \dots\dots\dots (27)$$

$$= \frac{V_{dc}}{f_c \cdot L_a} - (t_1 - t_2) \left(\frac{V_{as}^{MG,i}}{L_a} + \frac{dI_{as}^{*MG,i}}{dt} \right) = 4H_B \dots\dots\dots (28)$$

Substitute Equation (26) in Equation (27), and on simplification, we arrive at Equations (29) and (30). The hysteresis band (HB) obtained in Equation (30) can be modulated at different instants of frequency to obtain the required gate pulses to the inverter of an *i*th microgrid.

$$\frac{V_{dc}}{f_c \cdot L_a} - \frac{L_a}{V_{dc} \cdot f_c} \left(\frac{V_{as}^{MG,i}}{L_a} + \frac{dI_{as}^{*MG,i}}{dt} \right) = 4H_B \dots\dots\dots (29)$$

$$H_B = \left\{ \frac{V_{dc}}{4f_c \cdot L_a} \left(1 - \frac{L_a^2}{V_{dc}^2} \left[\frac{V_{as}^{MG,i}}{L_a} + \frac{dI_{as}^{*MG,i}}{dt} \right]^2 \right) \right\} \dots\dots\dots (30)$$

4.0 Results and Discussion

The power quality using the proposed FHCC-based inverter for urban community microgrids is investigated using characteristics such as voltage swell/sag, voltage imbalance, frequency deviations, and total harmonic distortion (THD). All parameters for the proposed FHCC-based inverter for cluster microgrids have been determined and compared to the conventional inverters for cluster microgrids following IEEE/IEC standards. Two conventional inverters are considered for the study: one is a traditional ST-PWM-based inverter, and the second is an FSV-PWM-based inverter. MATLAB/Simulink 2021, a software with a runtime version of 9.1 for Windows 64 bit, is used to model the planned interconnected microgrid system as well as individual microgrids that are connected to the utility grid.

4.1 Analysis of Voltage Characteristics

A pure sinusoidal voltage waveform must be maintained in the microgrid cluster system; otherwise, a distorted waveform will result in unequal voltage distribution. A test load, i.e., a high impedance load, contains more reactance, e.g., 50 kW + j200 kVAR, is applied to the system considered for study to observe the voltage waveform shape. Figure 8 shows the shape of the voltage waveform produced at the PCC of cluster microgrids. From the result, it is observed that the proposed FHCC-based inverter produces a pure sinusoidal waveform when compared with the conventional ST-PWM-based inverter, which contains small distortions in its voltage waveform.

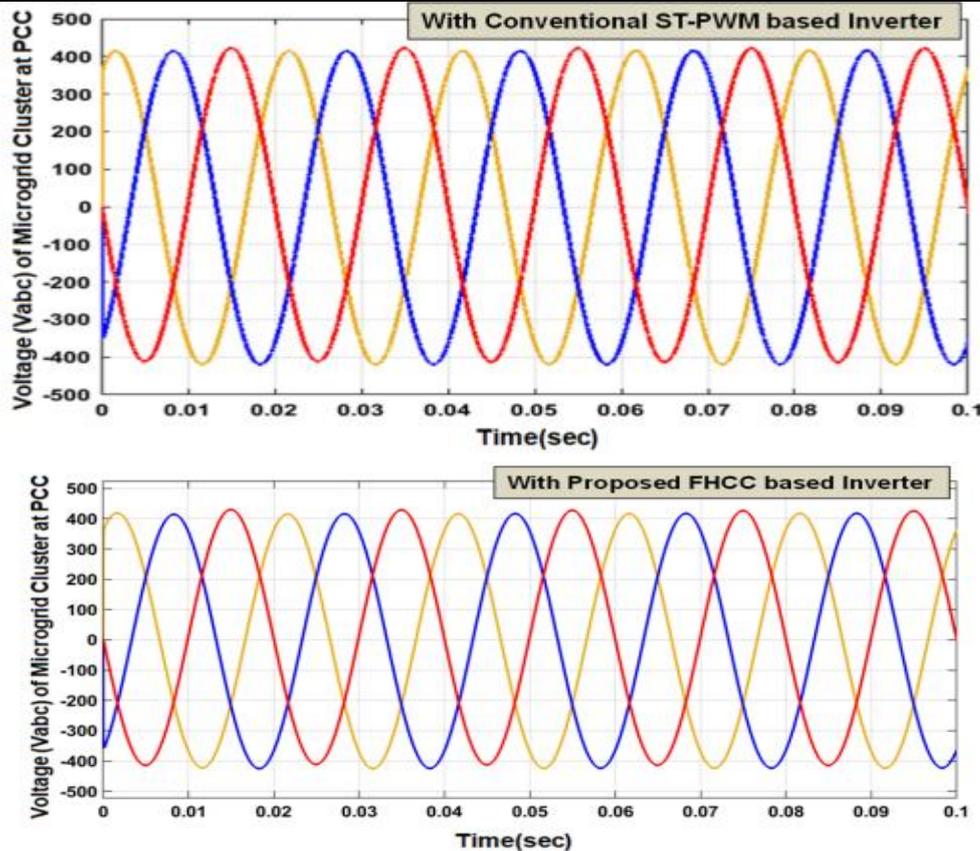


Figure 8. Voltage waveform at PCC of cluster microgrids with ST-PWM and FHCC-based inverters.

Furthermore, voltage sag occurs when the current flowing through the impedance (source) changes abruptly, and sudden application of load or faults drains large amounts of source currents, lowering the voltage. When there is a voltage swell, removing the load quickly causes the source currents to drop, causing the voltage to rise. To observe the voltage sag, apply a single line to ground (SLG) fault near to the PCC of cluster microgrids in phase A from 0.1 s to 0.2 s with a base load of 275 kW. The Simulink results with ST-PWM and FHCC-based inverters are shown in Figure 9

From results, it is observed that when SLG fault is applied at 0.1 s, the voltage is dropped to 230 V in the case when THE ST-PWM-based inverter is used, and the voltage is dropped to 300 V in the case when the proposed FHCC-based inverter is used. Similarly, the sag in the case of THE ST-PWM-based inverter is observed as 42.5%, and for the FHCCbased inverter, it is observed as 25%. Further, the swell in cluster microgrids is obtained by disconnecting a portion of baseload, e.g., 75% of 275 kW from 0.3 s to 0.4 s. From the results shown in Figure 9, the swell in the cluster microgrids with ST-PWM inverter is 10 V and with the proposed inverter is 7 V. Similarly, arc furnace load is applied from 0.15 s to 0.25 s to observe the sag in cluster microgrids by considering the base load of 250 kW. The corresponding results are as shown in Figure 10, from which it is observed that the sag is 43.37% using an ST-PWM-based inverter, while it is 27.7% with the FHCC-based inverter.

In order to study the voltage imbalances through positive and negative sequence components, inject a load of $j30$ kVAR (inductive load) in phase A at 0.12 s along with a

base load of 275 kW + j50 kVAR and a test load of j35 kVAR. The results are shown in Figure 11, from which it is observed that the voltage imbalance with the ST-PWM-based inverter is 3.06% and with the FHCC-based inverter is 2.86%. Further, the flicker impact on the system voltage was also studied. Flicker is defined as a visual impression caused by a light stimulus whose spectral component distribution changes over time. Both the ST-PWM-based inverter and the FHCC-based inverter for cluster microgrids at PCC produce short-term flicker sensations without spectral correction as shown in Figure 12. All the results that were obtained through simulations are consolidated as given in Table 3 along with standard requirements defined by IEEE/IEC/EN.

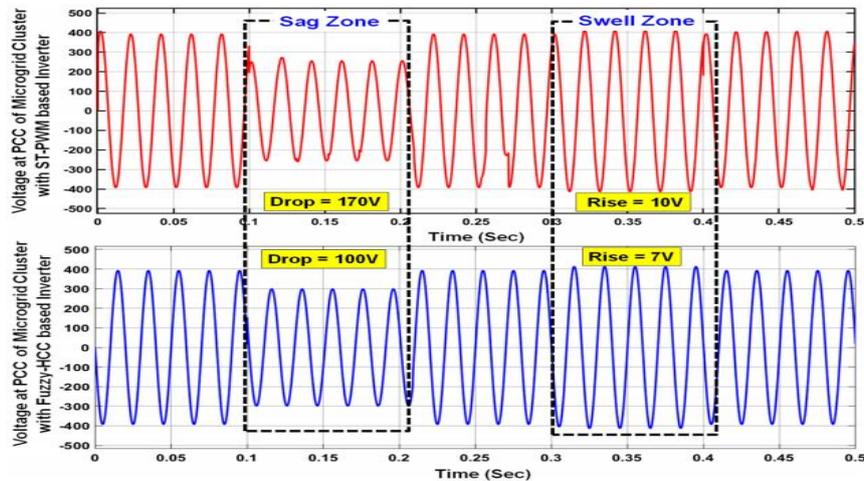


Figure 9. Voltage sag/swell obtained at PCC of cluster microgrids with conventional ST-PWM and proposed FHCC-based inverters subjected to large impedance load

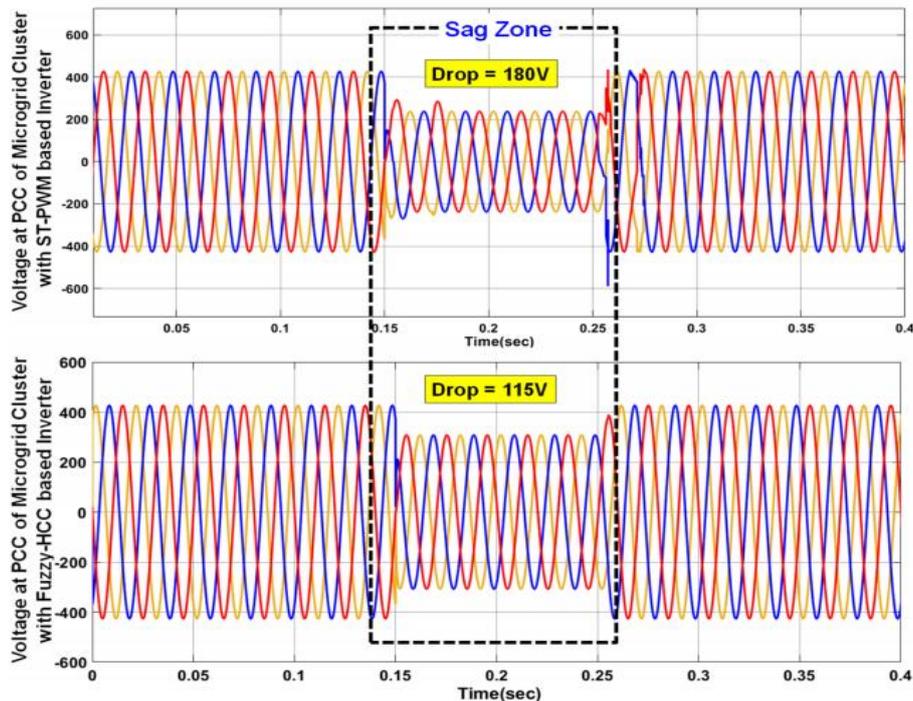


Figure 10. Voltage sag obtained at PCC of cluster microgrids with conventional ST-PWM- and proposed FHCC-based inverters subjected to arc furnace load.

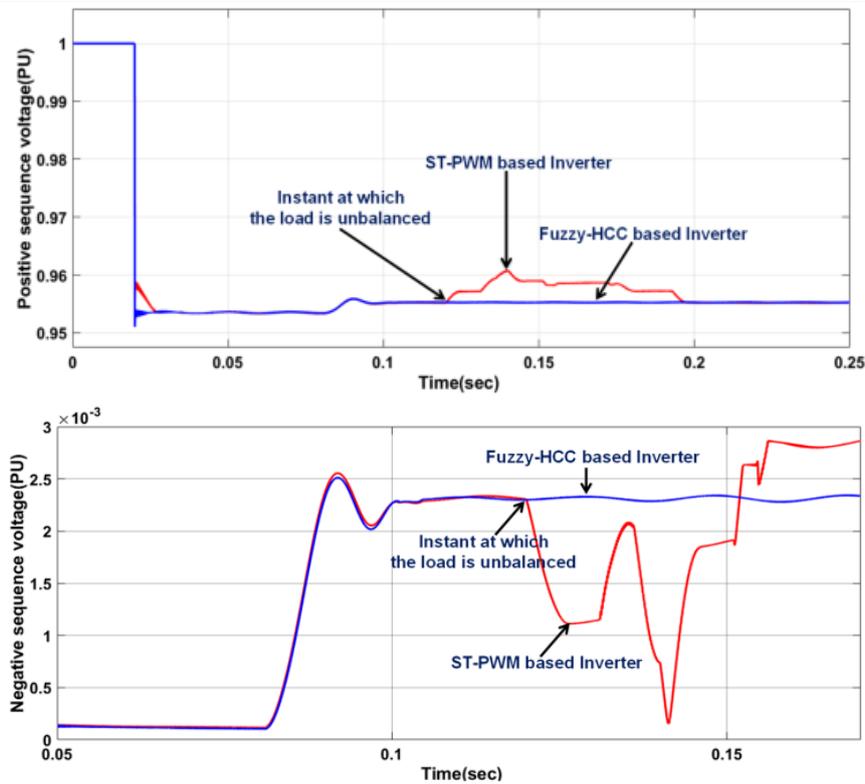


Figure 11. Voltage imbalances through positive and negative sequence voltages obtained at PCC of cluster microgrids with conventional ST-PWM- and proposed FHCC-based inverters.

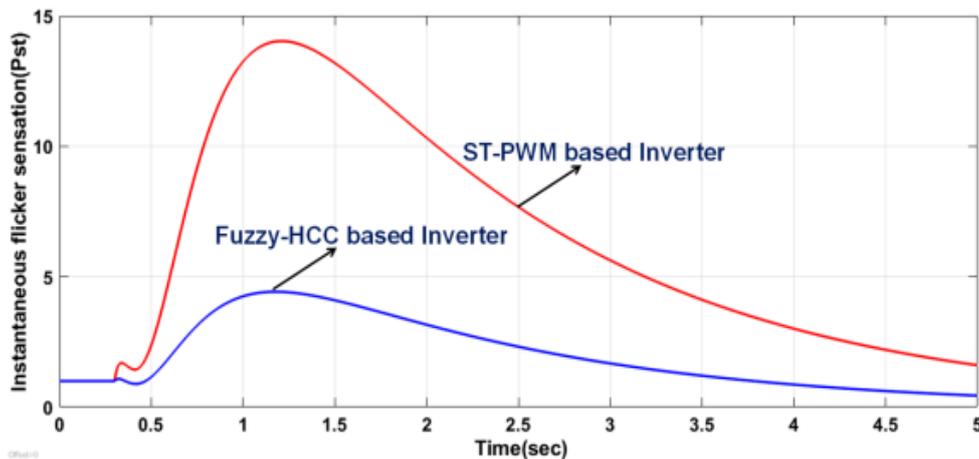


Figure 12. Flicker sensation of cluster microgrids at PCC with both with conventional ST-PWM- and proposed FHCC-based inverters.

5.2 Analysis of Frequency Characteristics

When the cluster microgrids are operated in grid-connected/autonomous mode, frequency is a critical parameter that must be monitored and managed continually. To observe the variation in frequency response for large impedance loads, apply a constant impedance of $275 \text{ kW} + j150 \text{ kVAR} - j50 \text{ kVAR}$ in the cluster microgrids from 0 s to 0.7 s. The results obtained from the simulation are shown in Figure 13. From these results, it is identified that the settling time (0.55 s) obtained with the ST-PWM-based inverter is

more when compared with the settling time (0.3 s) obtained with the FHCC-based inverter.

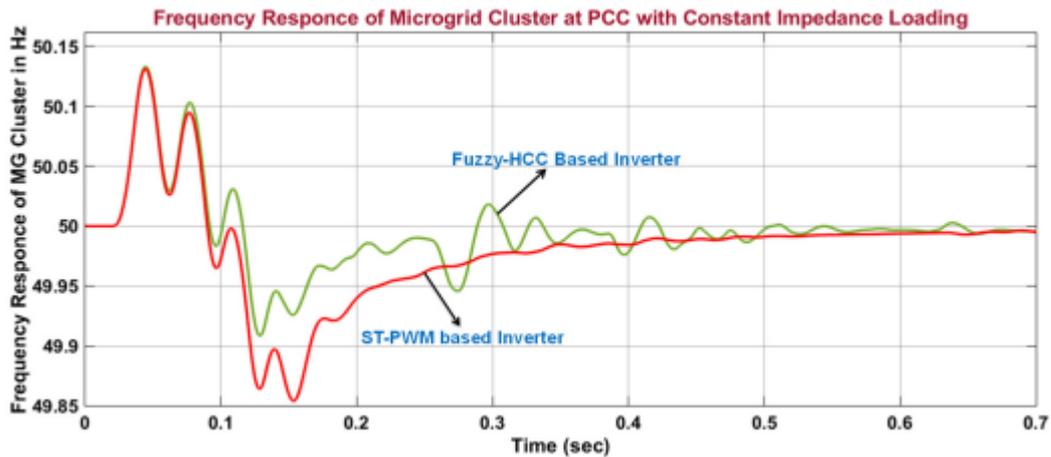


Figure:13 Frequency response of cluster micro grids at PCC with conventional ST-PWM- and proposed FHCC-based inverter subjected to large impedance

At the same time, to observe the variation in frequency response for large reactive loads, apply a load of 125 kW + j200 kVAR j175 kVAR in the cluster microgrids and follow the below given testing procedure:

Switch the inductive load ON at 0.5 s and OFF at 0.8 s;

Switch the capacitive load ON at 1.3 s and OFF at 1.6 s;

Observe deviation in case of dynamic loading.

The corresponding simulation results are shown in Figure 14. From this, it is identified that the deviation is less with the FHCC-based inverter when compared with the ST-PWM-based inverter. The summary of quantitative frequency characteristics is mentioned in Table 3

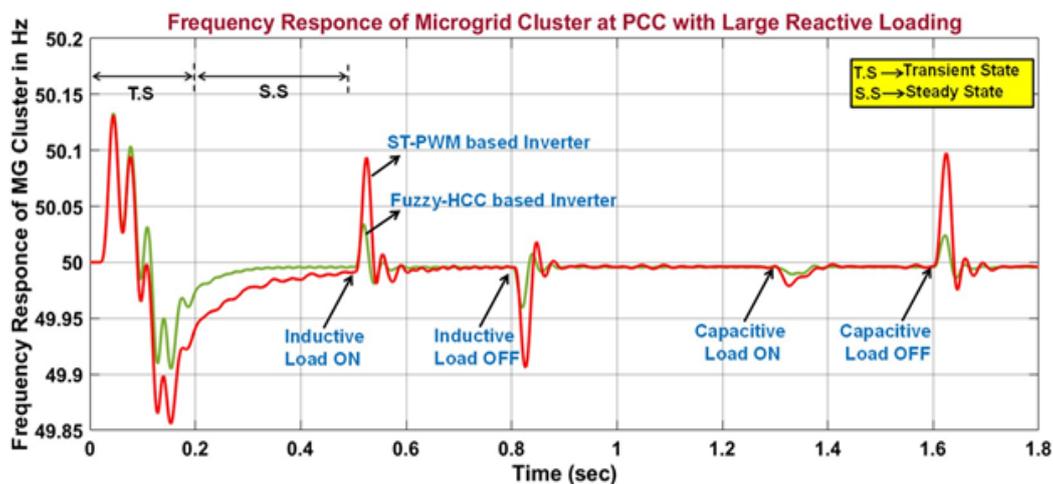


Figure:14 Frequency response of cluster micro grids at PCC with conventional ST-PWM- and proposed FHCC-based inverters subjected to large reactive load.

5.3 Analysis of THD

Total harmonic distortion (THD) is caused by nonlinear/high reactive loads in the system. To observe the effect of large resistive load on the system, the loads are injected into the system as follows: First, 250 kW is applied from 0.04 s to 0.1 s; 500 kW is applied from 0.08 s to 0.16 s; and 400 kW is applied from 0.12 s to 0.2 s. Similarly, to observe the effect of large reactive load on the system, the loads are injected into the system as follows: Switch ON j125 kVAR load at 0.12 s, and already connected 125 kVAR load must be switched OFF at 0.12 s along with the baseload of 275 kW.

5.0 Conclusions

The proposed FHCC-based inverter is easy to build, as it does not require any clamping diodes and capacitors when compared to conventional configurations. This further reduces the switching losses. The proposed inverter employs fuzzy logic, which reduces the complexity in mathematical formulation. From results, the FHCC-based inverter: Reduces the voltage sag to 25% when compared with conventional ST-PWM-based inverter (42.5%) and recent FSV-PWM-based inverter (29.5%) when the system is subjected to single line-to-ground fault. Similarly, it reduces the voltage sag to 27.7% when compared with conventional ST-PWM-based inverter (43.37%) and recent FSV-PWM-based inverter (38.6%) when subjected to arcing loads; Reduces the voltage swell to 2.5% when compared with conventional ST-PWM-based inverter (4.76%) and recent FSV-PWM-based inverter (2.73%) when subjected to sudden disconnection of major part (75%) of the system load; Reduces the voltage unbalance to 2.46% when compared with conventional ST-PWM-based inverter (3.08%) and recent FSV-PWM-based inverter (2.87%) when subjected to large reactive loads injected into the system; Reduces the settling time of the system to 0.35 s when compared with conventional ST-PWM-based inverter (0.55 s) and recent FSV-PWM-based inverter (0.42 s); Reduces the frequency deviation of the system to 0.3% when compared with conventional ST-PWM-based inverter (1%) and recent FSV-PWM-based inverter (0.45%); Reduces the total harmonic distortion of the system to 3.03% when compared with conventional ST-PWM-based inverter (4.29%) and recent FSV-PWM-based inverter (3.85%) when subjected to large resistive loading. Similarly, it reduces the total harmonic distortion of the system to 2.59% when compared with conventional ST-PWM-based inverter (6.38%) and recent FSV-PWM-based inverter (4.73%) for large reactive loading.

6.0 References

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